PRELIMINARY DATA SHEET: CKRF3511MM34

L TO S BAND LOW NOISE AMPLIFIER

Features :

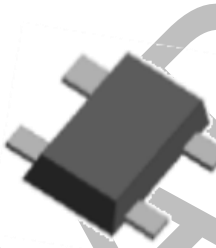
- Low noise figure and high associated gain
  NF=0.45dB Typ., Ga=16.5dB Typ.
  @Vdd=3.0V, Idd=7mA, f=1.575GHz

Description :

- Low Noise and High Gain
- On chip Bias supply circuit
- On chip ESD protection diode

Applications :

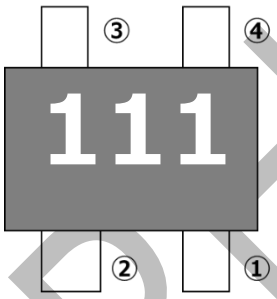
- Low Noise Amplifire IC for Global Navigation Satellite Systems (GNSS) like GPS, GLONASS, Beidou and Galileo
- Low Noise Amplifire IC for Satellite Radio (SDARS, DMB, etc.) Antenna
- Low Noise Amplifier for Microwave Communication



Package :

- Flat-lead 4-pin thin-type super minimold package

PIN Configuration :



PIN No.	PIN Name
1	Source
2	OUT
3	Source
4	IN

Ordering Information :

Part Number	Order Number	Package	Marking	Supplying Form
CKRF3511MM34-C2	CKRF3511MM34-C2	Flat-lead 4-pin thin-type super minimold package	111	・Embossed 8 mm wide ・Pin 1 (Source), Pin 2 (OUT) Face the perforation side of the Tape ・Qty 15Kpcs/reel

Absolute Maximum Ratings :

Parameter	Symbol	Rating	Unit
Supply Voltage	Vdd	4.0	V
Input Power	Pin	-5	dBm
Operating Ambient Temperature	T_A	-45~+85	℃
Storage Temperature	Tstg	-55~+150	℃

Recommended Operating Range :

(T_A=+25℃, unless otherwise specified)

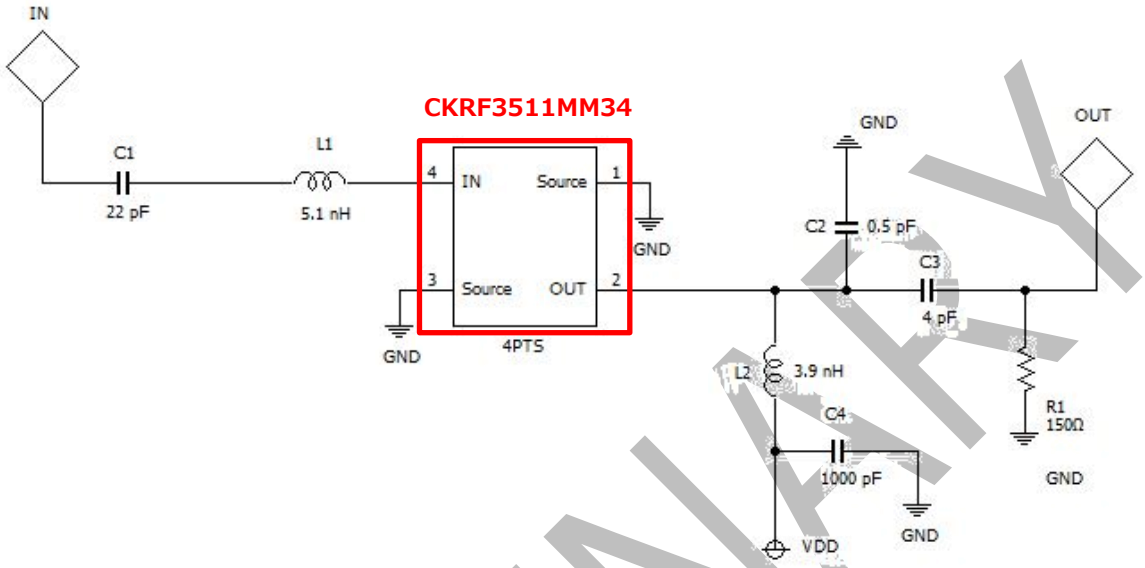
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	Vdd	2.7	3.0	3.3	V
Supply Current	Idd	TBD	7.0	TBD	mA

Electrical Characteristics :

(T_A=+25℃, unless otherwise specified)

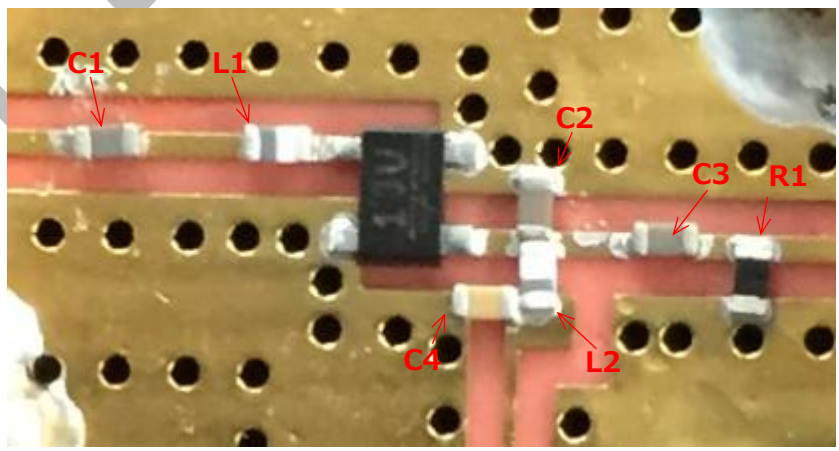
Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Power Gain	Gain	Vdd=3.0V, Idd=7mA, f=1.575GHz	TBD	16.5	-	dB
Noise Figure	NF		-	0.45	TBD	dB
Input 3rd Order Intercept Point	IIP3	Vdd=3.0V, Id=7mA, f=1.575GHz	-	TBD	-	dBm
Output Power at 1dB Compression Point	$P_{O(1dB)}$	Vdd=3.0V, Idd=7mA (Non-RF) f=1.575GHz	-	11.0	-	dBm

Application Circuit :

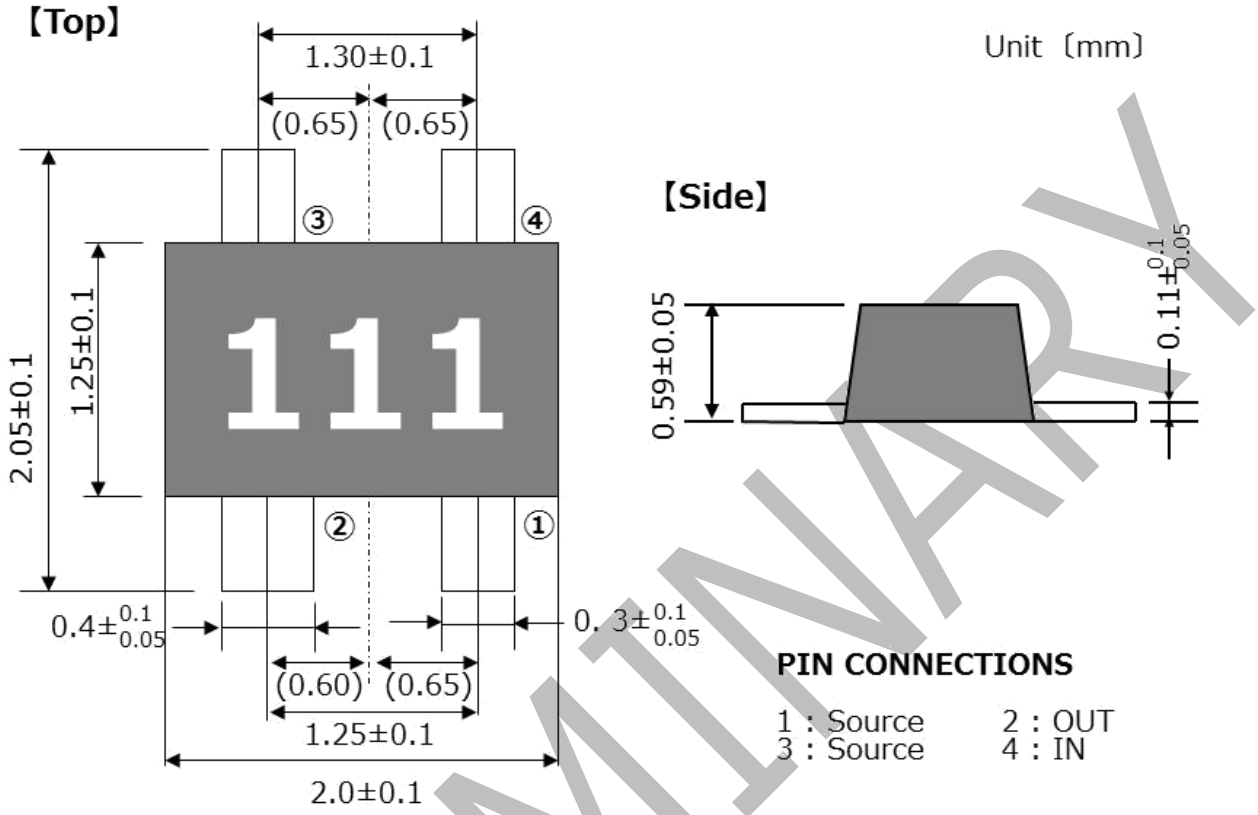


Evaluation Board Information :

<PCB>
FR-4
t = 0.2 mm
εr = 4.30
tan delta = 0.016



Package Dimensions :



Recommended Soldering Conditions :

Recommended Soldering Conditions are provided on the CDK Web site.
[Original Products] → [Low Noise GaAsFET] → [Design Support] → [others]
URL http://www.en.cdk.co.jp/products/highfrequency/rf/designsupport/index.html

[CAUTION]

- All information included in this document is current as of the date this document is issued. Such information, however, is subject to change without any prior notice.
- You should not alter, modify, copy, or otherwise misappropriate any CDK product, whether in whole or in part.
- CDK does not assume any liability for infringement of patents, copyrights, or other intellectual property rights of third parties by or arising from the use of CDK products or technical information described in this document. No license, express, implied or otherwise, is granted hereby under any patents, copyrights or other intellectual property rights of CDK or others.
- Descriptions of circuits, software and other related information in this document are provided only to illustrate the operation of semiconductor products and application examples. You are fully responsible for the incorporation of these circuits, software, and information in the design of your equipment. CDK assumes no responsibility for any losses incurred by you or third parties arising from the use of these circuits, software, or information.
- CDK has used reasonable care in preparing the information included in this document, but CDK does not warrant that such information is error free. CDK assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.
- Although CDK endeavors to improve the quality and reliability of its products, semiconductor products have specific characteristics such as the occurrence of failure at a certain rate and malfunctions under certain use conditions. Please be sure to implement safety measures to guard them against the possibility of physical injury, and injury or damage caused by fire in the event of the failure of a CDK product, such as safety design for hardware and software including but not limited to redundancy, fire control and malfunction prevention, appropriate treatment for aging degradation or any other appropriate measures
  Because the evaluation of microcomputer software alone is very difficult, please evaluate the safety of the final products or system manufactured by you.
- Please use CDK products in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive.
  CDK assumes no liability for damages or losses occurring as a result of your noncompliance with applicable laws and regulations.
- This document may not be reproduced or duplicated, in any form, in whole or in part, without prior written consent of CDK.
- Please contact a CDK if you have any questions regarding the information contained in this document or CDK products, or if you have any other inquiries.

[Caution in the gallium arsenide (GaAs) product handling]

This product uses gallium arsenide (GaAs) of the toxic substance appointed in laws and ordinances. GaAs vapor and powder are hazardous to human health if inhaled or ingested.

- Do not dispose in fire or break up this product.
- Do not chemically make gas or powder with this product.
- When discard this product, please obey the law of your country.
- Do not lick the product or in any way allow it to enter the mouth.

[CAUTION]

Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

CHUO DENSHI KOGYO CO., LTD
3400 Kooyama, Matsubase, Uki-City,
Kumamoto 869-0512, Japan
Tel : +81-964-32-2730
Fax : +81-964-32-3549
URL : http://www.en.cdk.co.jp/

Contact info for inquiries
Electronic Devices Division Sales and Planning Department
Tel : +81-964-32-2750
E-mail : info@cdk.co.jp
FAX : +81-964-32-3549